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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/753,081

01/08/2004

Shinji Tanabe

OKI.633

5416

20987

7590

10/18/2004

VOLENTINE FRANCO, & WHITT PLLC  
ONE FREEDOM SQUARE  
11951 FREEDOM DRIVE SUITE 1260  
RESTON, VA 20190

EXAMINER

FENTY, JESSE A

ART UNIT

PAPER NUMBER

2815

DATE MAILED: 10/18/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

10/753,081 ✓

Applicant(s)

TANABE ET AL

Examiner

Jesse A. Fenty

Art Unit

2815

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 08 January 2004.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-19 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-12, 18 and 19 is/are allowed.
- 6) ☒ Claim(s) 13-17 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 01/08/04.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

## DETAILED ACTION

### *Claim Objections*

1. Claims 13 and 14 are objected to because of the following informalities:
  - a. Claim 13 recites the limitation "a second wiring" in line 8 of the claim. There is insufficient antecedent basis for this limitation in the claim. Appropriate correction is required.

### *Claim Rejections - 35 USC § 102*

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 13-17 are rejected under 35 U.S.C. 102(b) as being anticipated by Lin (U.S. Patent No. 6489656 B1).

In re claim 13, Lin (Fig. 1) discloses a semiconductor device, comprising:

A semiconductor chip (10) which has an electrode pad (16), wherein the semiconductor chip includes a first area<sup>1</sup> and a second area<sup>2</sup>;

An insulating layer (18) formed on the semiconductor chip, wherein the insulating layer has an opening aligned over the electrode pad;

A first external terminal (26) formed over the first area;

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<sup>1</sup> Analog functions are known to the prior art (column 1, lines 35-40).

<sup>2</sup> The area left of center can be designated the first area; and the area right of center can be designated a second area.

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A second external terminal (28) formed over the second area; and

A second wiring (36) formed on the insulating layer, wherein the second wiring electrically connects the electrode pad to the second external terminal via the opening; and

Wherein the first external terminal is electrically isolated from the semiconductor chip.

In re claim 14, Lin discloses the device of claim 13, wherein the second wiring is not formed over the first area.

In re claim 15, Lin discloses the device of claim 13, wherein the first external terminal and the second external terminal have a same shape.

In re claim 16, Lin (Fig. 2) discloses a semiconductor device, comprising:

A semiconductor chip (10);

A first insulating layer (18) formed on the semiconductor chip;

A shielding film (44) formed on the first insulating layer;

A second insulating layer (20) formed on the first insulating layer and on the shielding film; and

An inductor (40) and an external terminal (26) formed on the second insulating layer;

Wherein the shielding film is located under the inductor.

In re claim 17, Lin discloses the device of claim 16, wherein the shielding film is a magnetic material (column 10, lines 54-55).

***Allowable Subject Matter***

3. Claims 1-12, 18 and 19 are allowed.

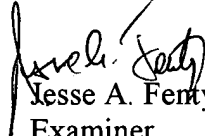
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*Conclusion*

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jesse A. Fenty whose telephone number is 571-272-1729. The examiner can normally be reached on 5/4-9 1st Fri. Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on 571-272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

  
Jesse A. Fenty  
Examiner  
Art Unit 2815